

BEST AVAILABLE COPYCustomer No.: 31561
Docket No.: 11835-US-PA
Application No.: 10/711,541**Amendments To Claims**

1. (currently amended) A method for responding a reading command in an automatically adaptive memory card, comprising:

determining whether a present reading address of the reading command is same as a previous reading address; ;

wherein when said present reading address is same as said previous reading address; ,

outputting a response and outputting a data in a data-lag mode to output said data after said response is outputted ; ; and

wherein when said present reading address is not same as said previous reading address; and ,

updating said previous reading address to be said present reading address, ~~output~~ outputting said response and ~~outputting~~ said data in a data-parallel mode to output said data regardless of whether ~~or not~~ said response is outputted.

2.(original) The method of claim 1, wherein said step of outputting said data in a data-lag mode includes outputting said data after said response is outputted and after a first predetermined time passes.

3. (original) The method of claim 2, further comprising a step of outputting

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Customer No.: 31561
Docket No.: 11835-US-PA
Application No.: 10/711,541

said data in said data-lag mode during a second predetermined time without determining whether said present reading address of said reading command is the same as said previous reading address after said data is outputted in said data-lag mode.

4. (original) The method of claim 3, further comprising a step of outputting said data in said data-lag mode without determining whether or not said present reading address of said reading command is the same as said previous reading address when it has determined for a plurality of times that said present reading address of said reading command is the same as said previous reading address.

5. (original) The method of claim 1, further comprising:

determining whether said reading command passes a cyclic redundancy check; and

stopping a step of processing said reading command when said reading command does not pass a cyclic redundancy check.

6. (original) The method of claim 1, further comprising a step of reading said data based on said reading command from a memory of said memory card during said step of outputting said response.

7. (original) The method of claim 1, wherein when said reading command is a multi-block reading command, further comprising:

BEST AVAILABLE COPYCustomer No.: 31561
Docket No.: 11835-US-PA
Application No.: 10/711,541

outputting a data in a next block of said data after outputting said data;
and stop outputting said data when said memory card receives a stop command.

8. (original) The method of claim 1, wherein said memory card is a SD
memory card.

9. (currently amended) A method for responding a reading command in an
automatically adaptive memory card, comprising:

outputting a response, wherein

when a present reading address of a reading command is same as a
previous reading address, a data is output in a data-lag mode to output said data
after said response is outputted, ; and

when said present reading address is not same as said previous reading
address, said previous reading address is updated to be said present reading
address and said data is output in a data-parallel mode to output said data
regardless of whether or not said response is outputted.

10. (original) The method of claim 9, further comprising: determining
whether said reading command passes a cyclic redundancy check, wherein when said
reading command does not pass the cyclic redundancy check, processing of said
reading command is stopped.